



MAIL STOP RCE
PATENT
8008-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA Conf. 2273

Application No. 10/812,282 Group 2815

Filed March 30, 2004 Examiner J. Jackson, Jr.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AMENDMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 29, 2008

Sir:

Further to the Notice of Appeal filed July 29, 2008, and in conjunction with the Request for Continued Examination (RCE) filed herewith, please amend this application as follows.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 9 of this paper.